

CYW9BTM2BASE1

INFINEON M.2 BT/BLE BASEBOARD

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Drawing Numbers

PCBA	121-90658-01
PCB	600-90658-01
FAB DRW	610-90658-01
ASSY DRW	620-90658-01
SCH DRW	630-90658-01

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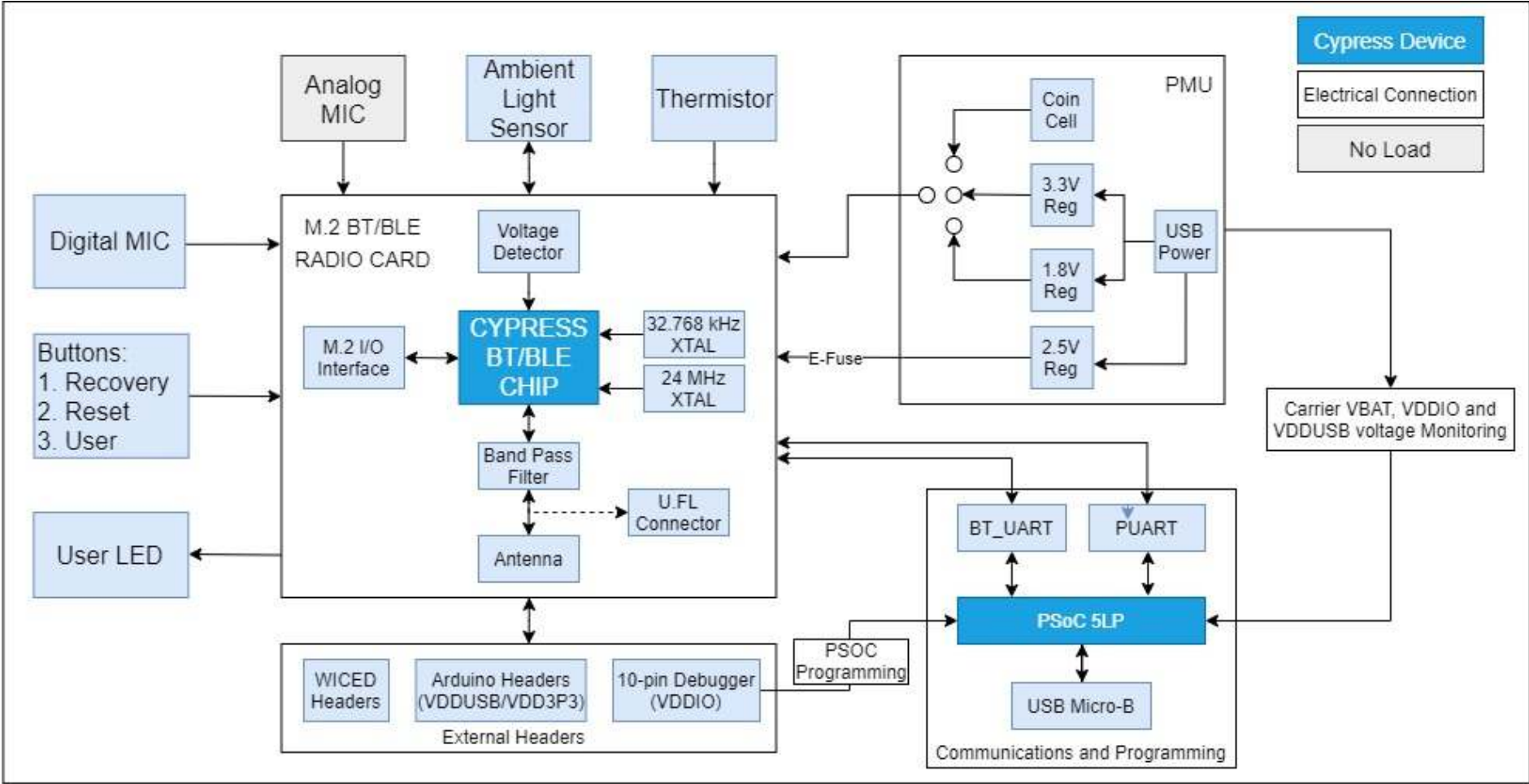


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Page Title: **Title Page**

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Block Diagram



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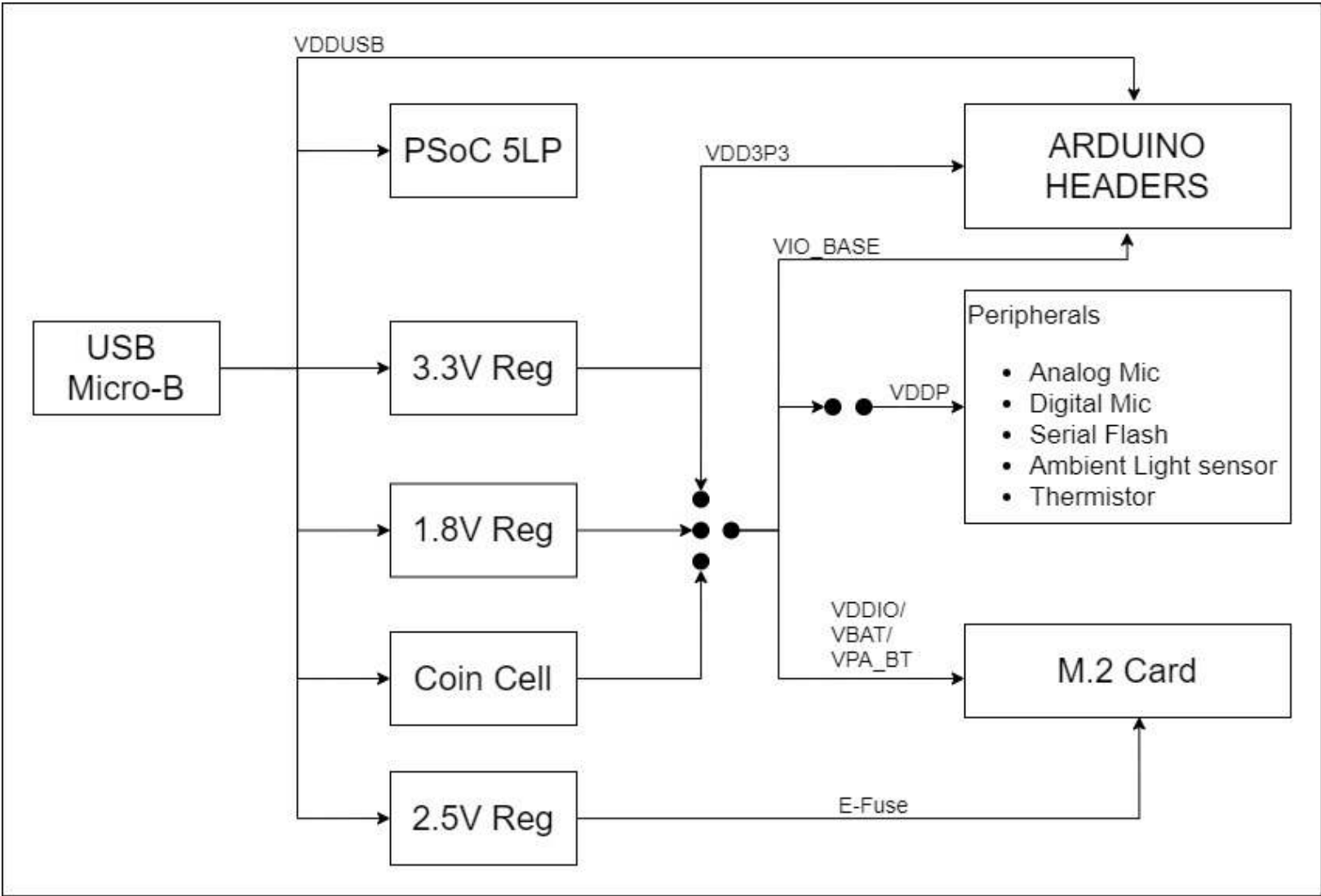


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Page Title: **Block Diagram**

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Power Management Unit Block Diagram



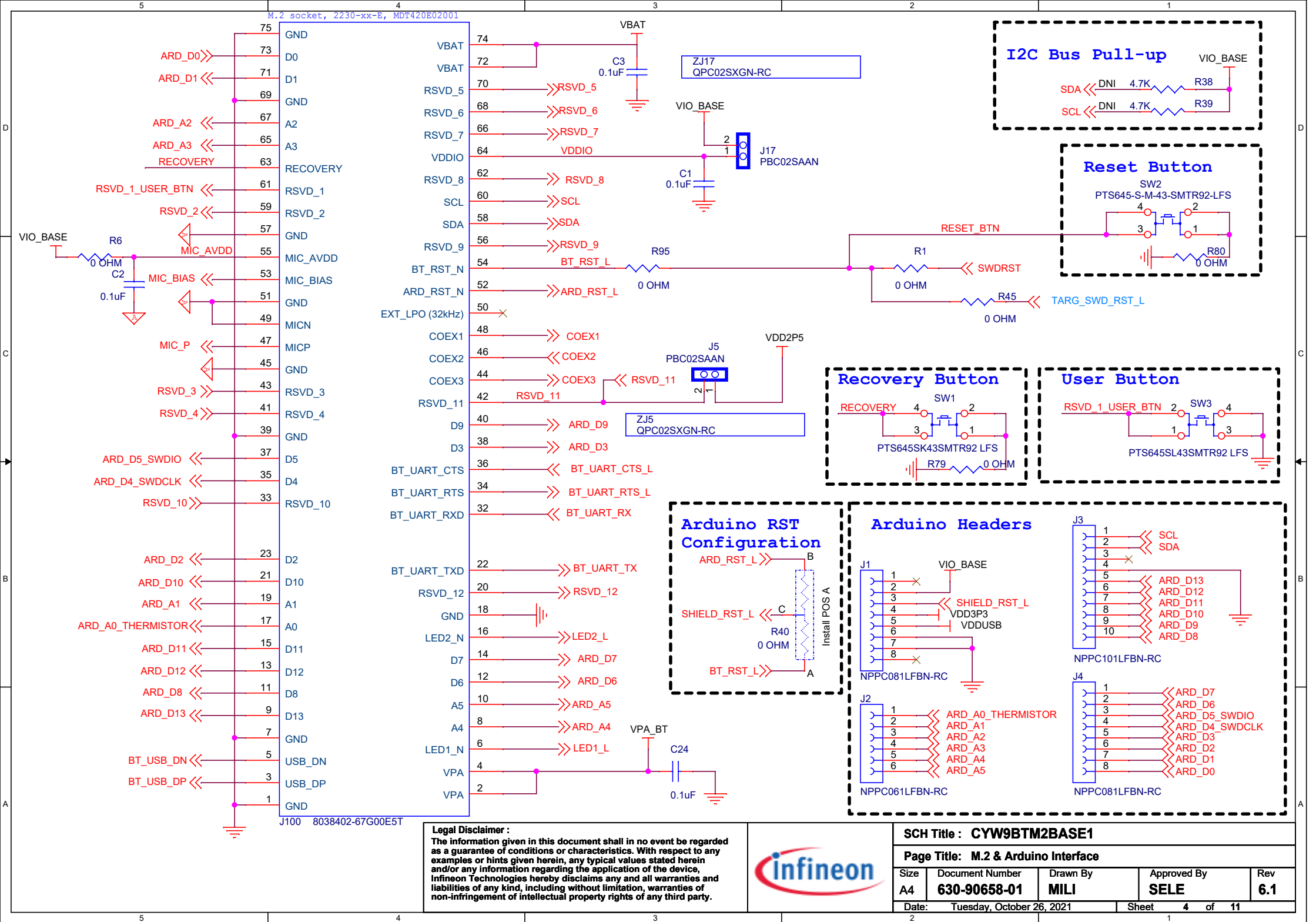
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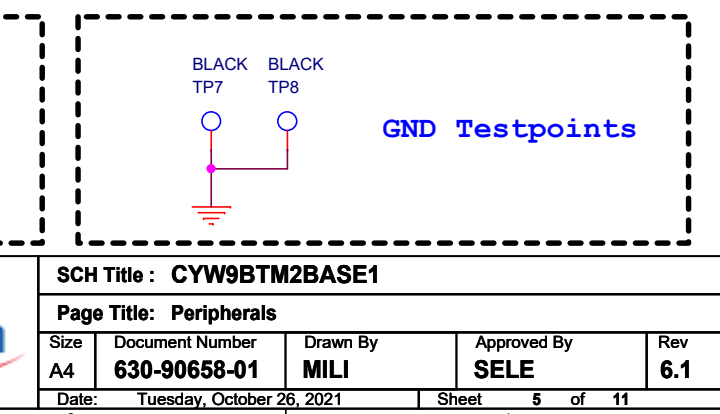
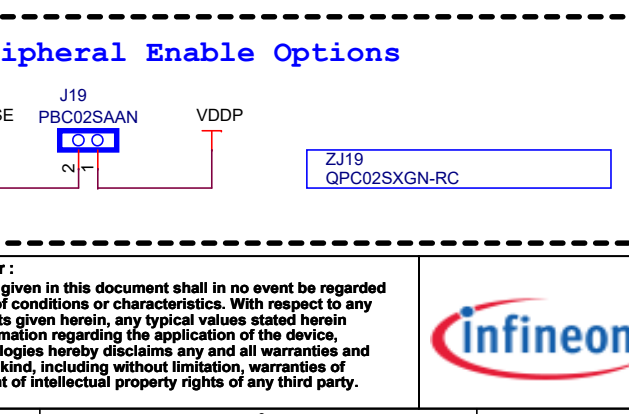
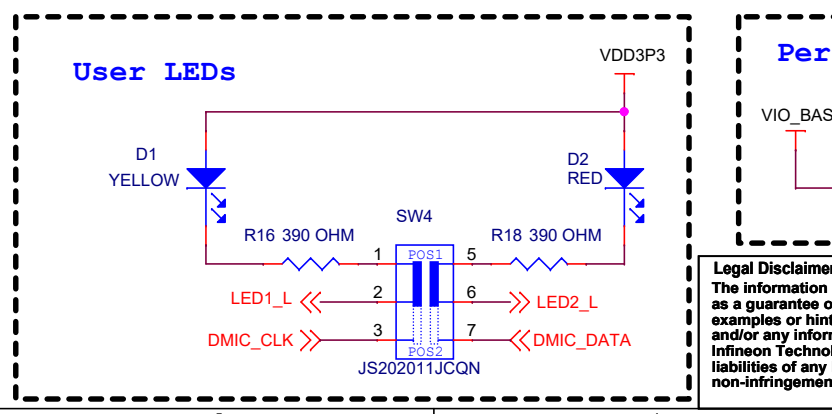
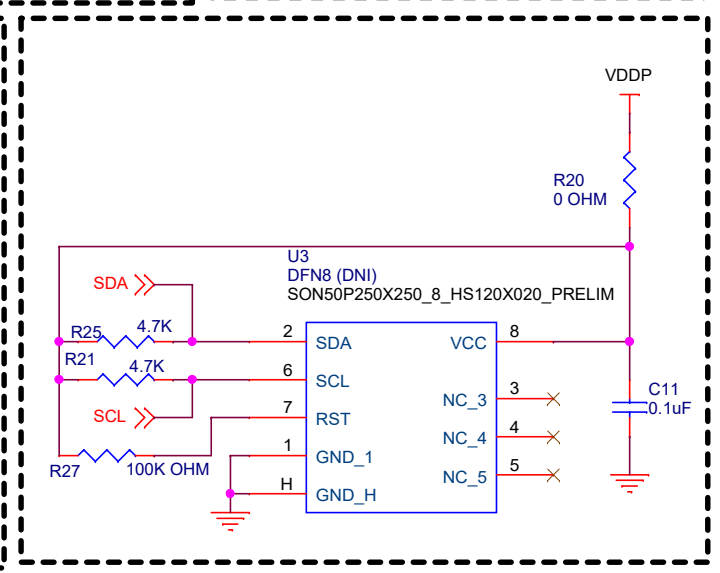
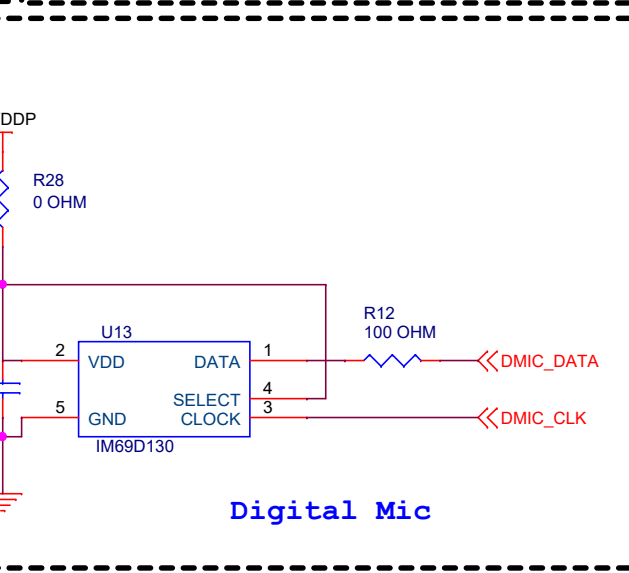
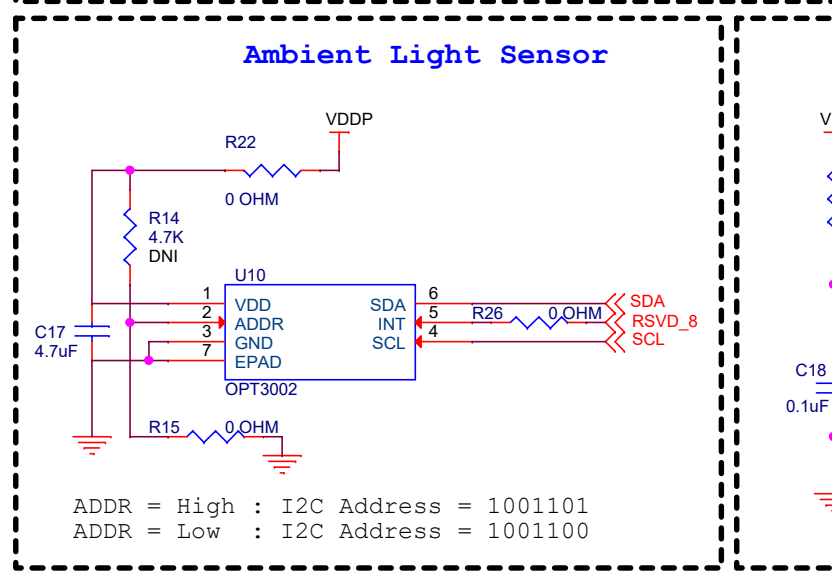
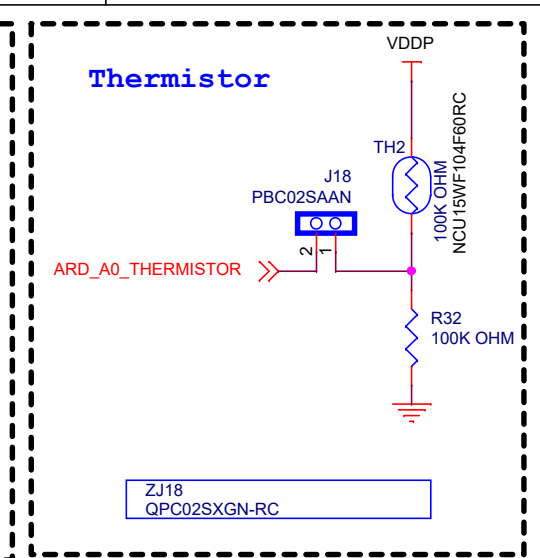
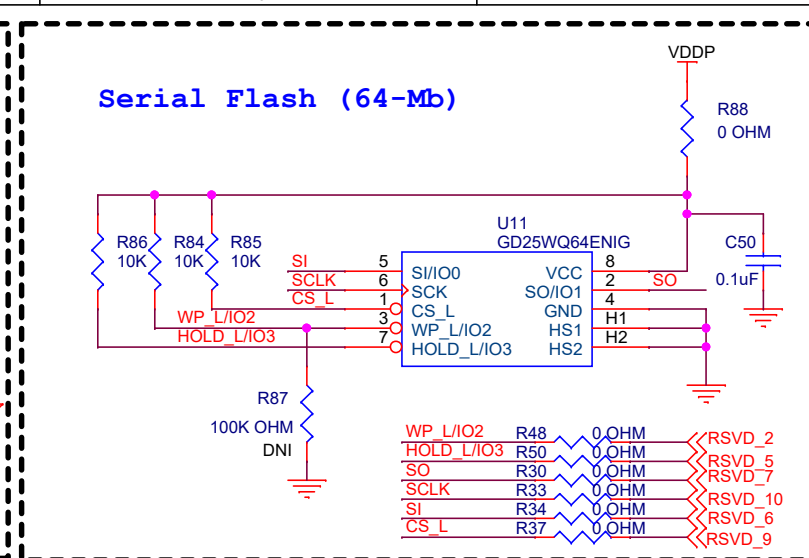
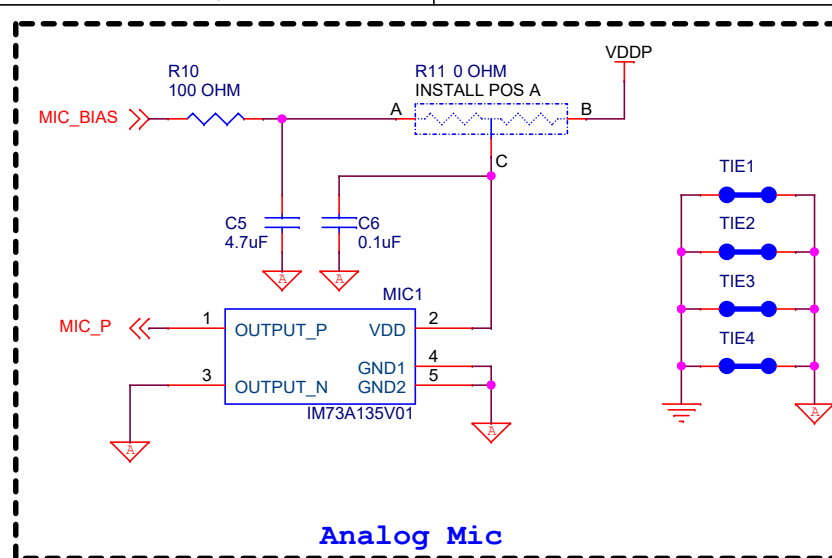


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Page Title: PMU Block Diagram

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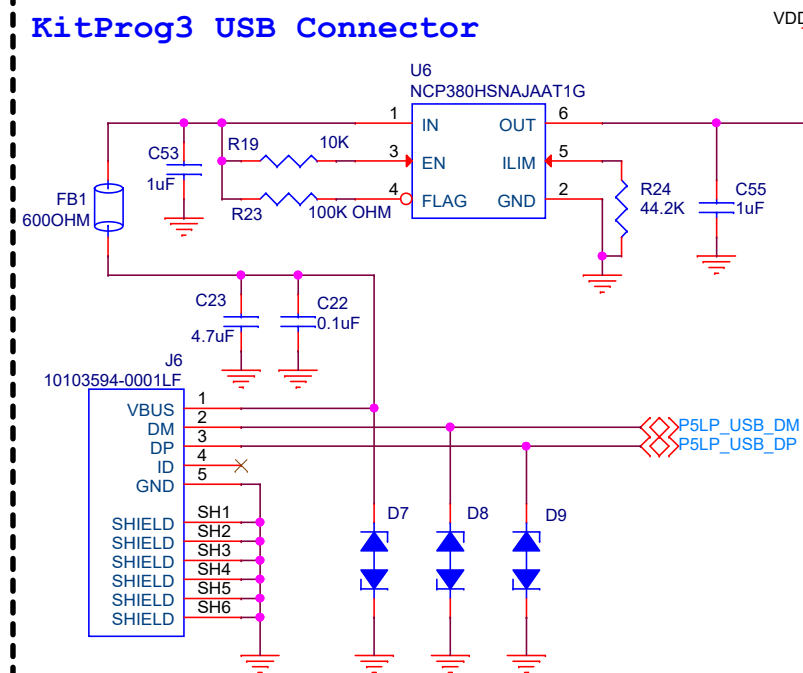


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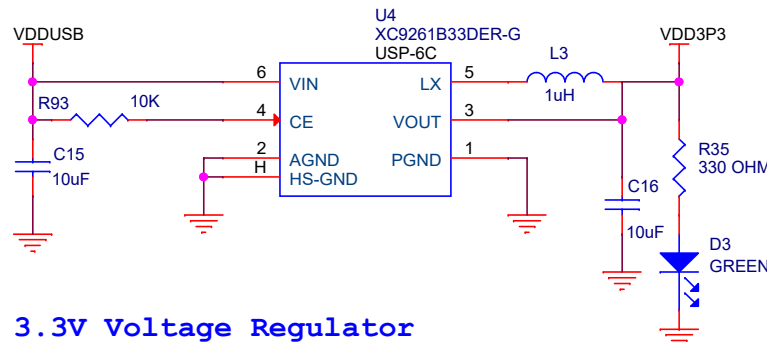


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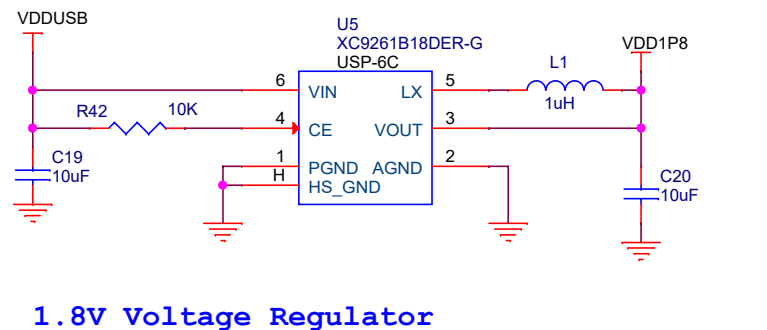
KitProg3 USB Connector



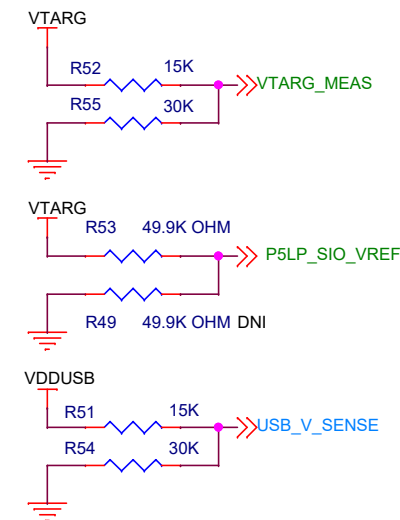
3.3V Voltage Regulator



1.8V Voltage Regulator



VOLTAGE MONITORING

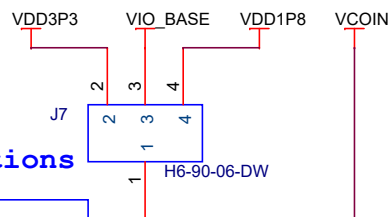


VIO_BASE Supply Options:

Short J7 Pos 2-3: Power VIO_BASE from VDD3P3
Short J7 Pos 3-4: Power VIO_BASE from VDD1P8
Short J7 Pos 1-3: Power VIO_BASE from Coin Cell

VIO_BASE Options

ZJ7-2-3
QPC02SXGN-RC

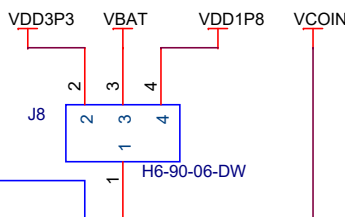


VBAT Supply Options:

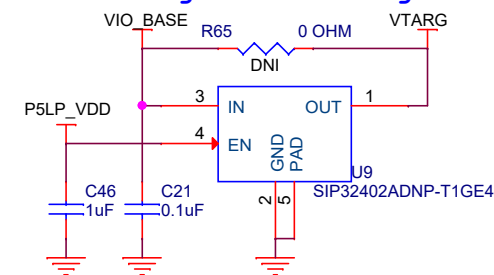
Short J8 Pos 2-3: Power VBAT from VDD3P3
Short J8 Pos 3-4: Power VBAT from VDD1P8
Short J8 Pos 1-3: Power VBAT from Coin Cell

VBAT Options

ZJ8-2-3
QPC02SXGN-RC



Voltage Monitoring

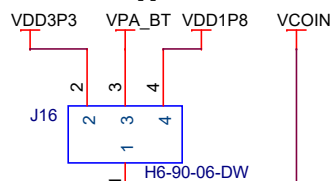


VPA_BT Supply Options:

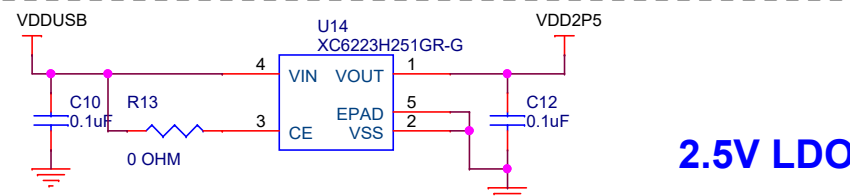
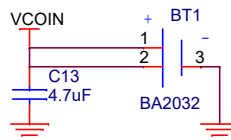
Short J16 Pos 2-3: Power VPA_BT from VDD3P3
Short J16 Pos 3-4: Power VPA_BT from VDD1P8
Short J16 Pos 1-3: Power VPA_BT from Coin Cell
See kit user guide for list of supported operation modes.

VPA_BT Options

ZJ16-2-3
QPC02SXGN-RC



CR2032 Coin Cell Holder



2.5V LDO

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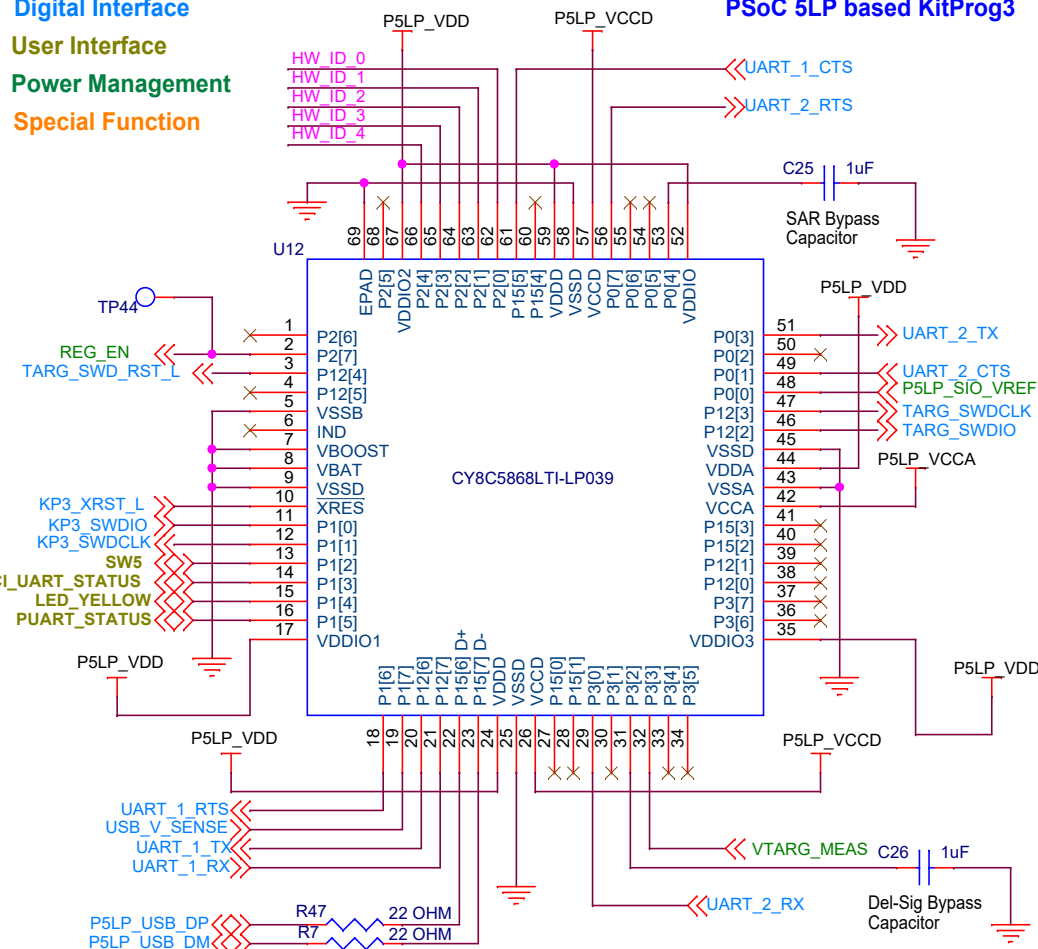
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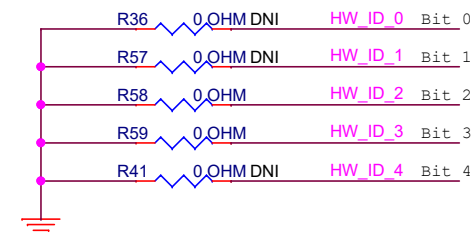
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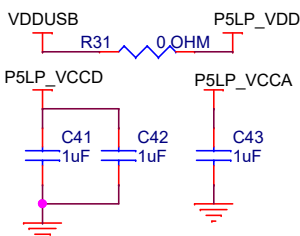


ID = 0x0C



Note: GND is read as binary "1" and floating as "0"

Supply for PSoC 5LP



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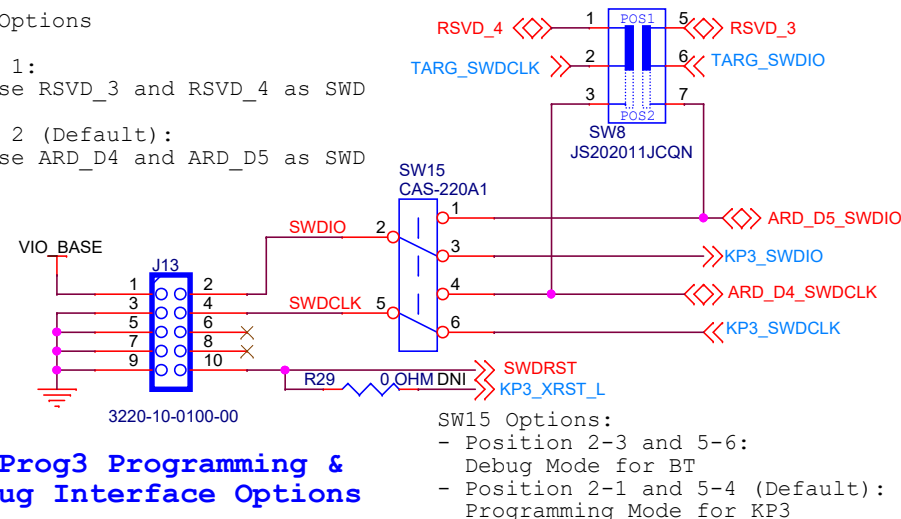
SW8 Options

Pos. 1:

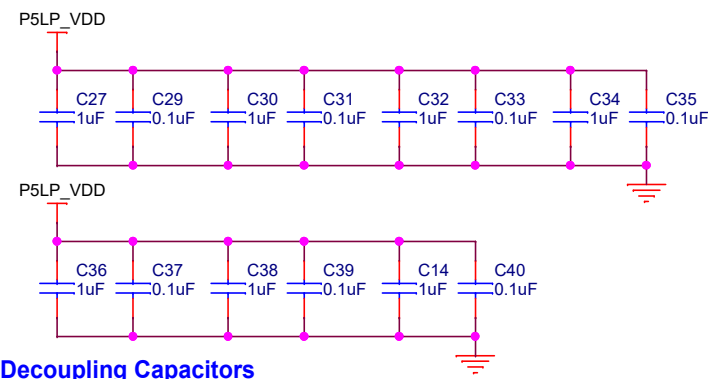
- Use RSVD 3 and RSVD 4 as SWD

Pos. 2 (Default):

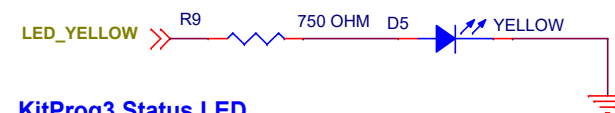
- Use ARD D4 and ARD D5 as SWD



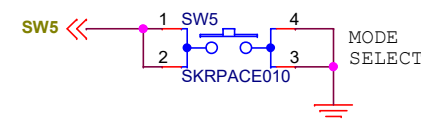
KitProg3 Programming & Debug Interface Options



Decoupling Capacitors



KitProg3 Status LED



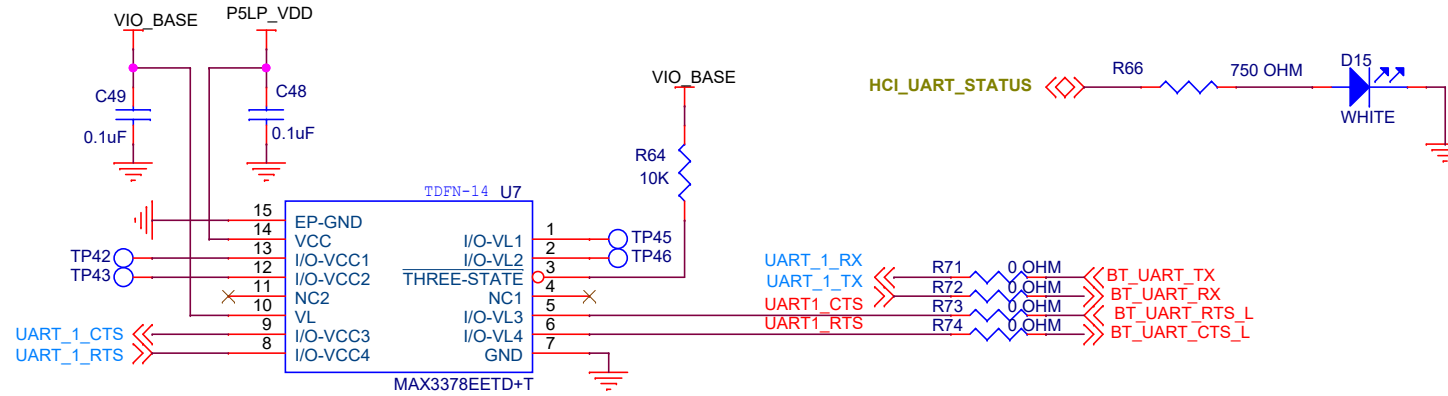
PSoC 5LP Mode Select Switch

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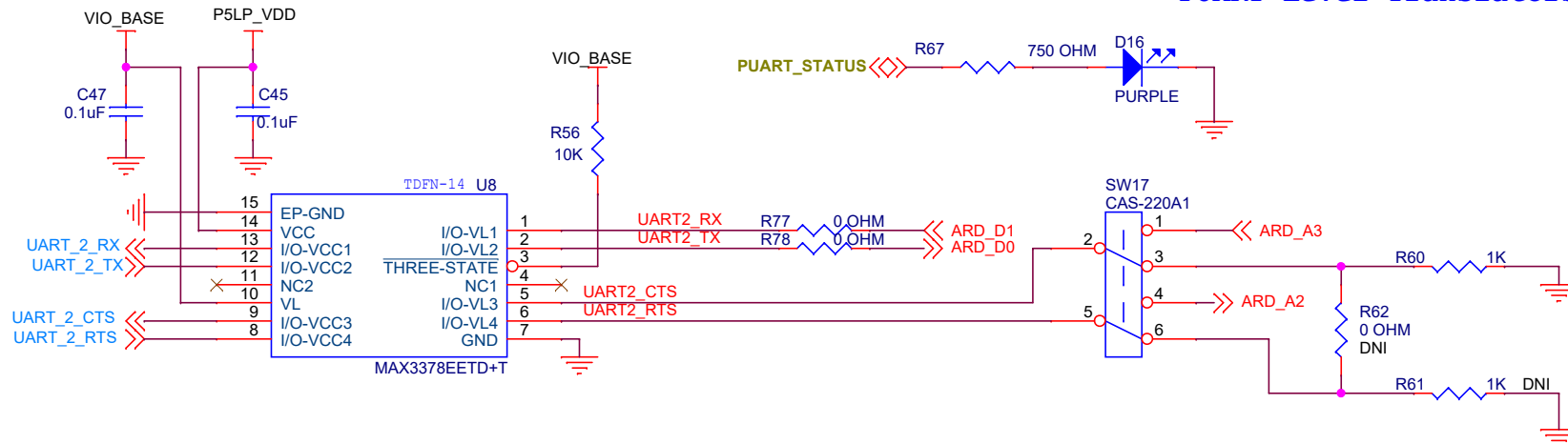
Page Title: PSoC 5LP

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HCI UART Level Translator



PUART Level Translators



SW17 Options:

- Position 2-3 and 5-6 (Default): Disable PUART flow control
- Position 2-1 and 5-4: Enable PUART flow control

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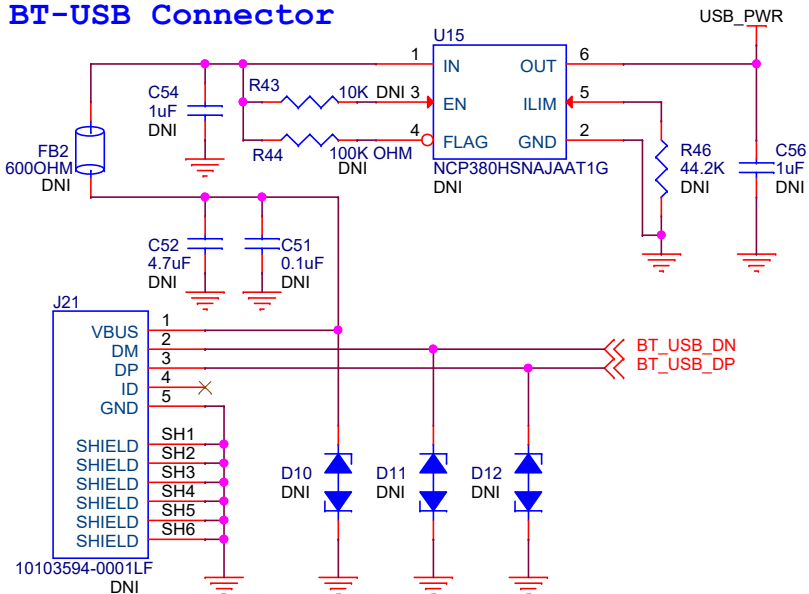


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BT-USB Connector



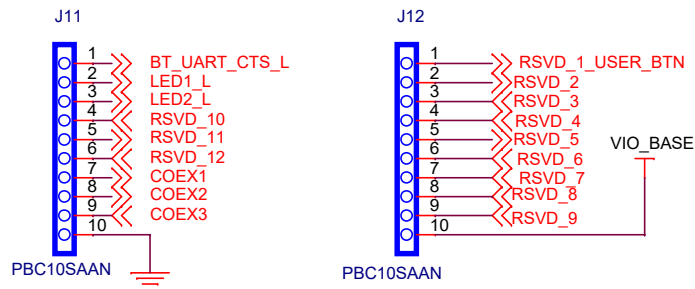
Rubber Feet Stand-offs

- MT1
SJ61A4
- MT2
SJ61A4
- MT3
SJ61A4
- MT4
SJ61A4

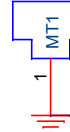
USB CABLE

- CBL1
750-90043-01

WICED HEADERS



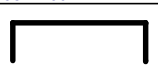
M1
S001-00001-A42-R
Standoff for M.2 Card



- ZH1
S001-00003
M.2 standoff screw

M.2 Connector Accessories

ACC6
298 SV001



USB CONNECTOR BRACKET

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REVISION HISTORY

REV	DESCRIPTION OF CHANGE	Orig. of Change	DATE
1.0	Initial Release	MILI	03/06/2019
1.1	DNI R96, Install 10K Ohm at R64	MILI	04/18/2019
2.0	Schematic updated to Cypress template. Removed PSoC5 unused testpoints and SW14. Removed PSoC5 custom app button and LED. Motion sensor replaced by ambient light sensor.	MILI	05/30/2019
3.0	R49 changed to DNI. Removed R66, R67, R68 and R69. SW4 and SW8 changed to slide switch. Arduino Digital Pins changed to "Dx" naming convention Corrected PUART_RX to D0 and PUART_TX to D1	MILI	07/16/2019
4.0	Added digital MIC, U13 Serial flash changed to 64Mbit	MILI	01/20/2020
5.0	Analog MIC (MIC1) changed to IM73A135V01 Digital MIC (U13) changed to IM69D130 Assigned RSVD2 to WP_L/IO2 and RSVD5 to HOLD_L/IO3 to support QSPI Added 2.5V LDO (U14) to support E-fuse. Assigned 2.5V Supply to RSVD_11 Added SW17 to enable/disable flow control on PUART Route BT_UART_CTS_L to J11-1 Added activity indicator LED for HCI UART and PUART, HWID changed to 0xE	MILI	05/24/2021
6.0	U10 Changed to OPT3002	MILI	07/27/2021
6.1	HWID Changed back to 0x0C (DNI R57)	MILI	10/26/2021

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